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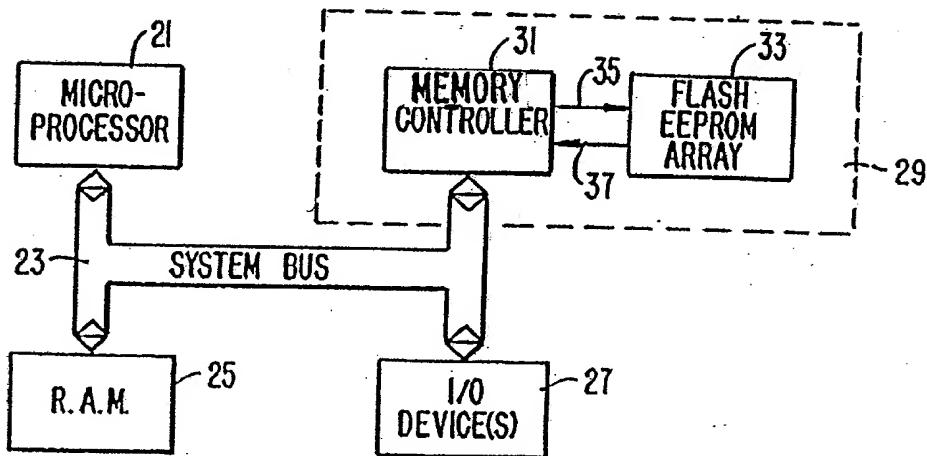


FIG._1A

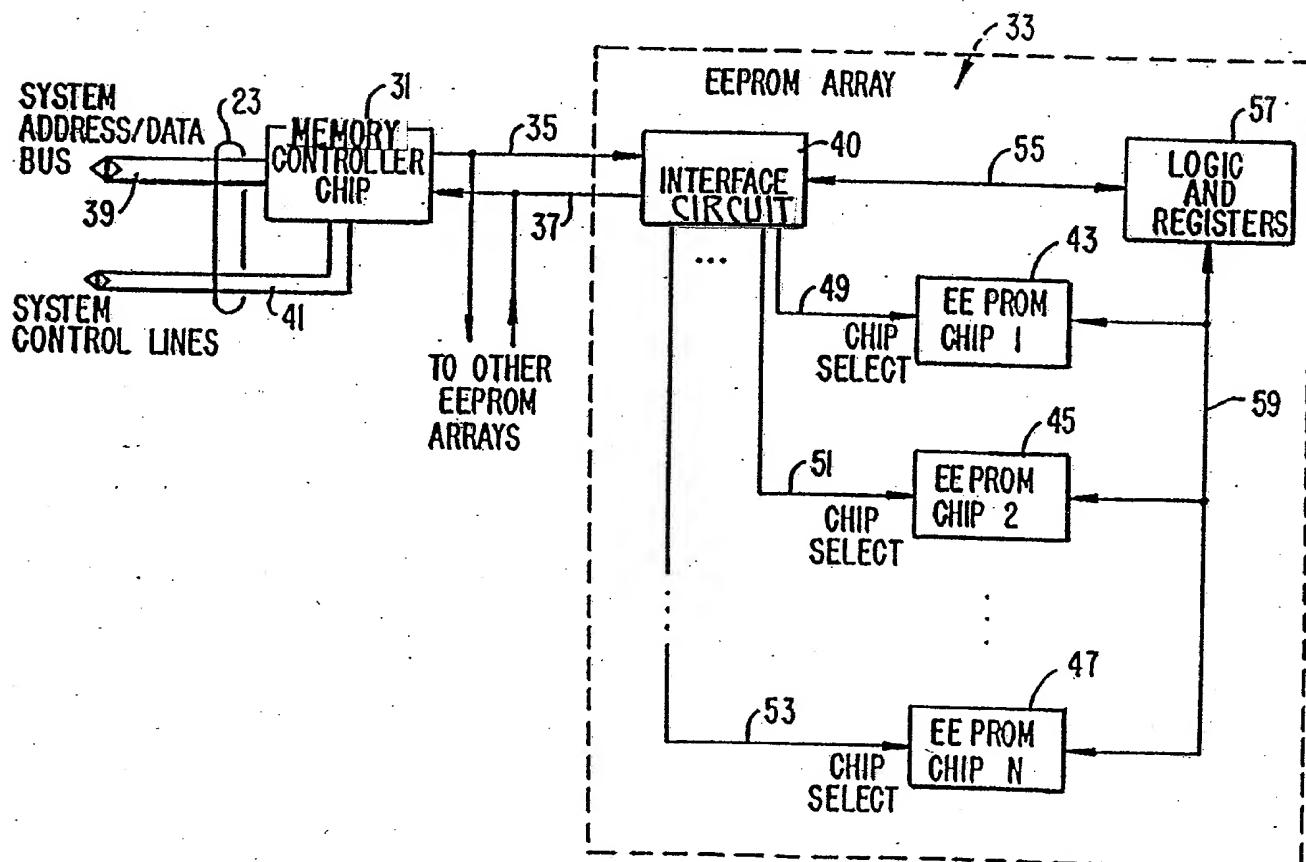


FIG._1B

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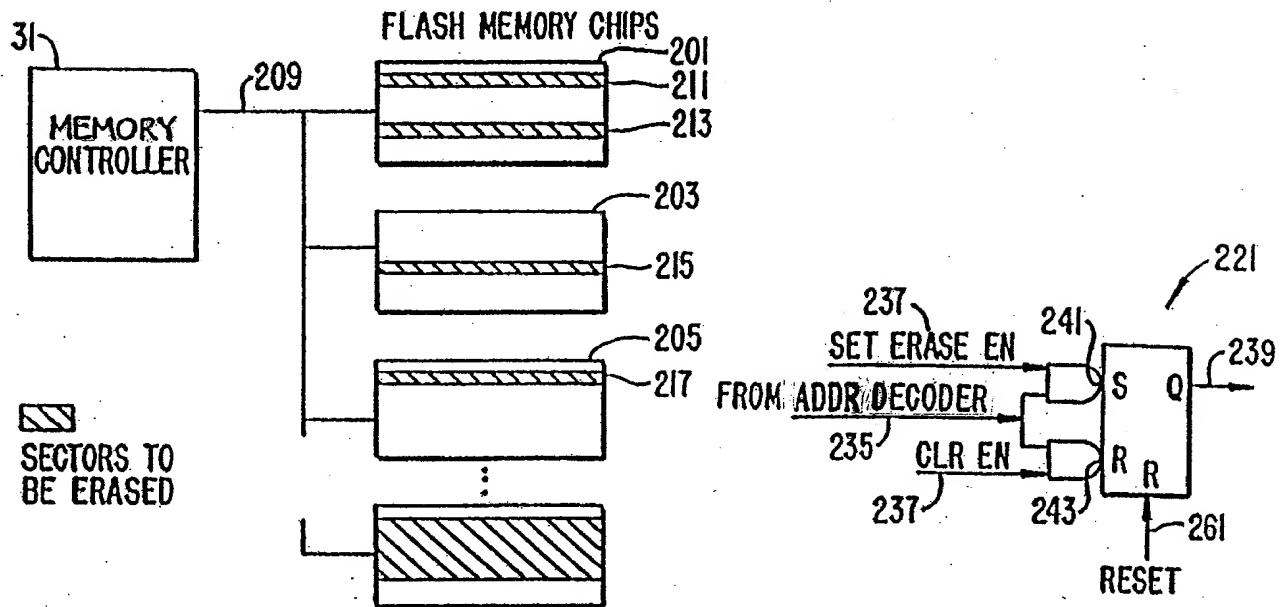


FIG.-2

FIG.-3B

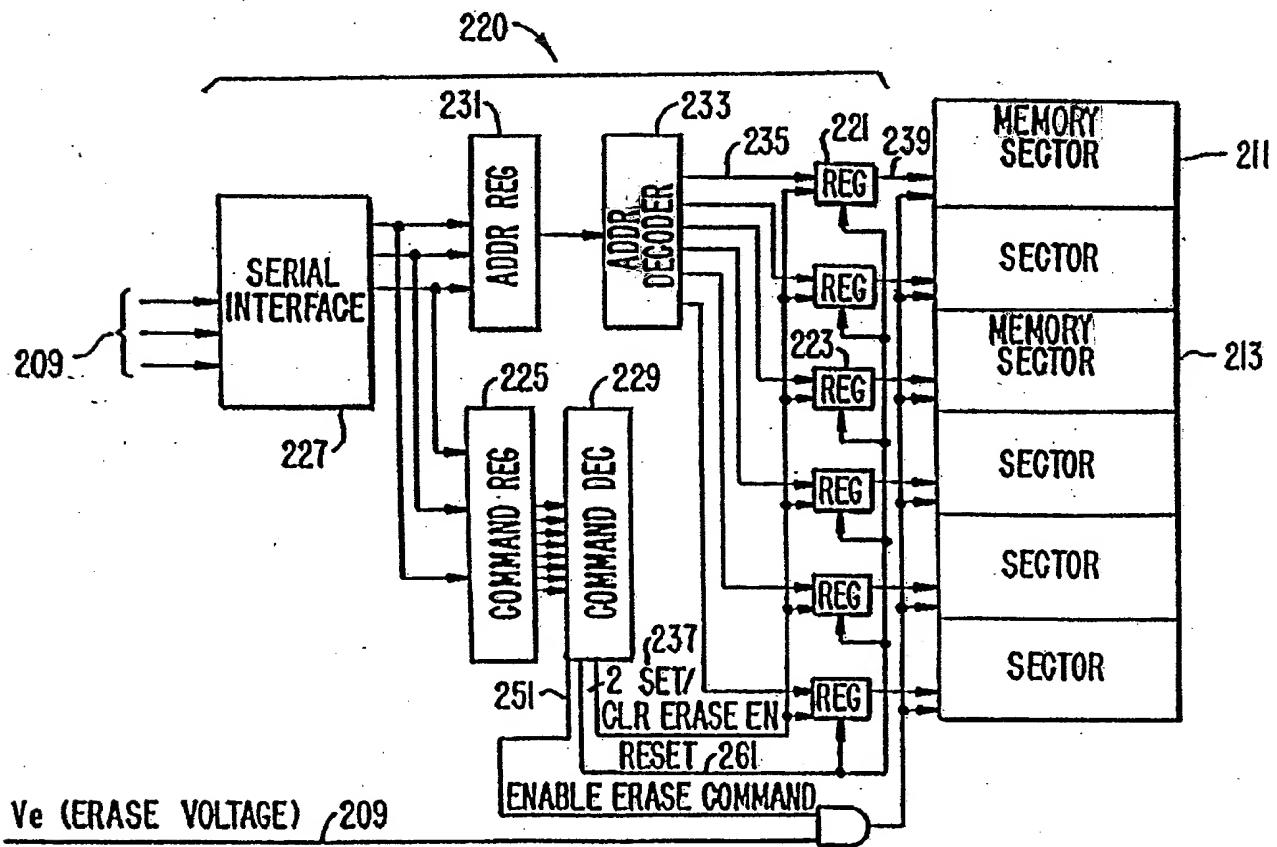
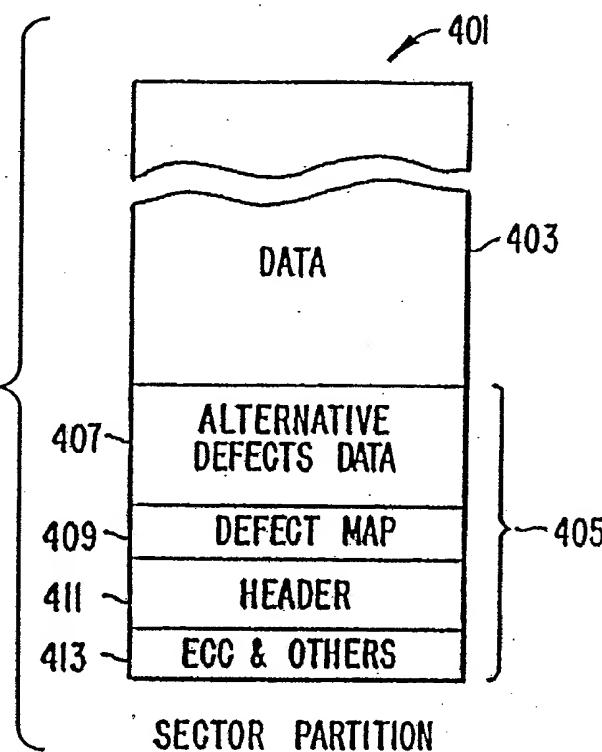


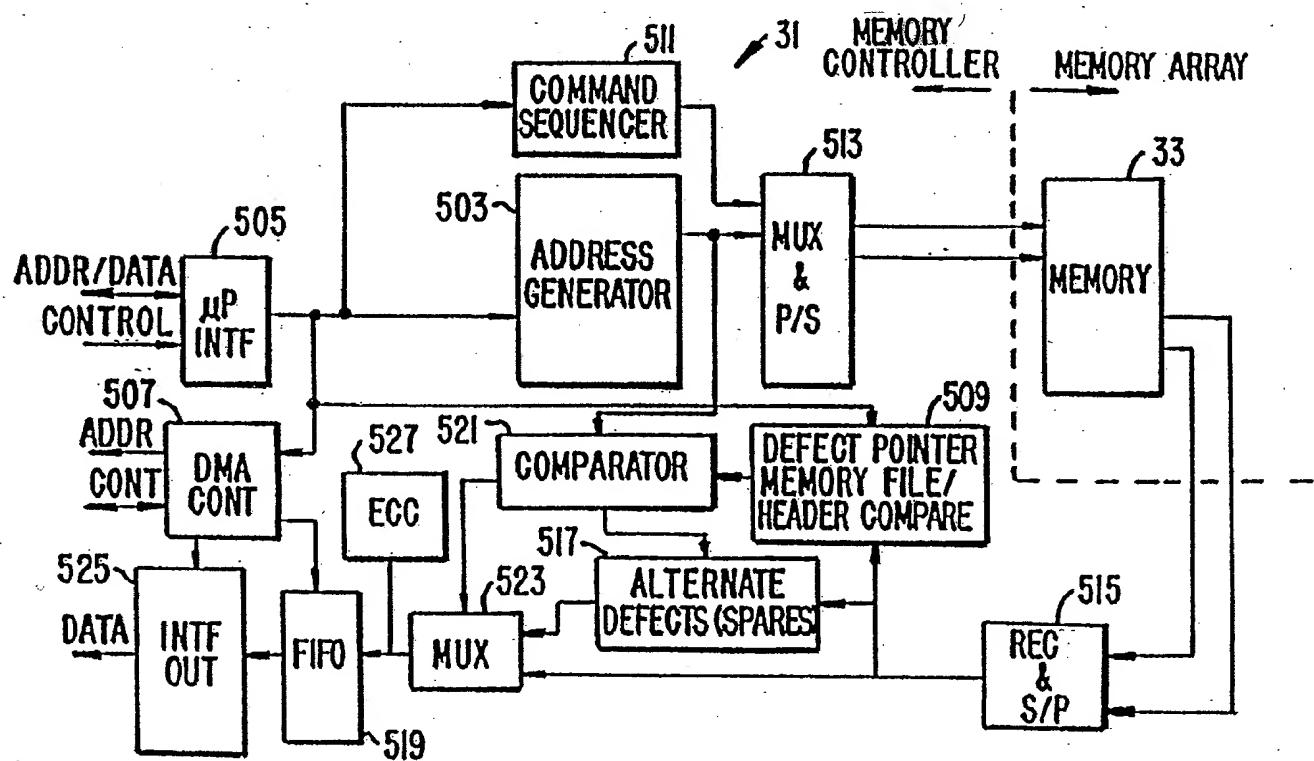
FIG.-3A

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FIG.-5



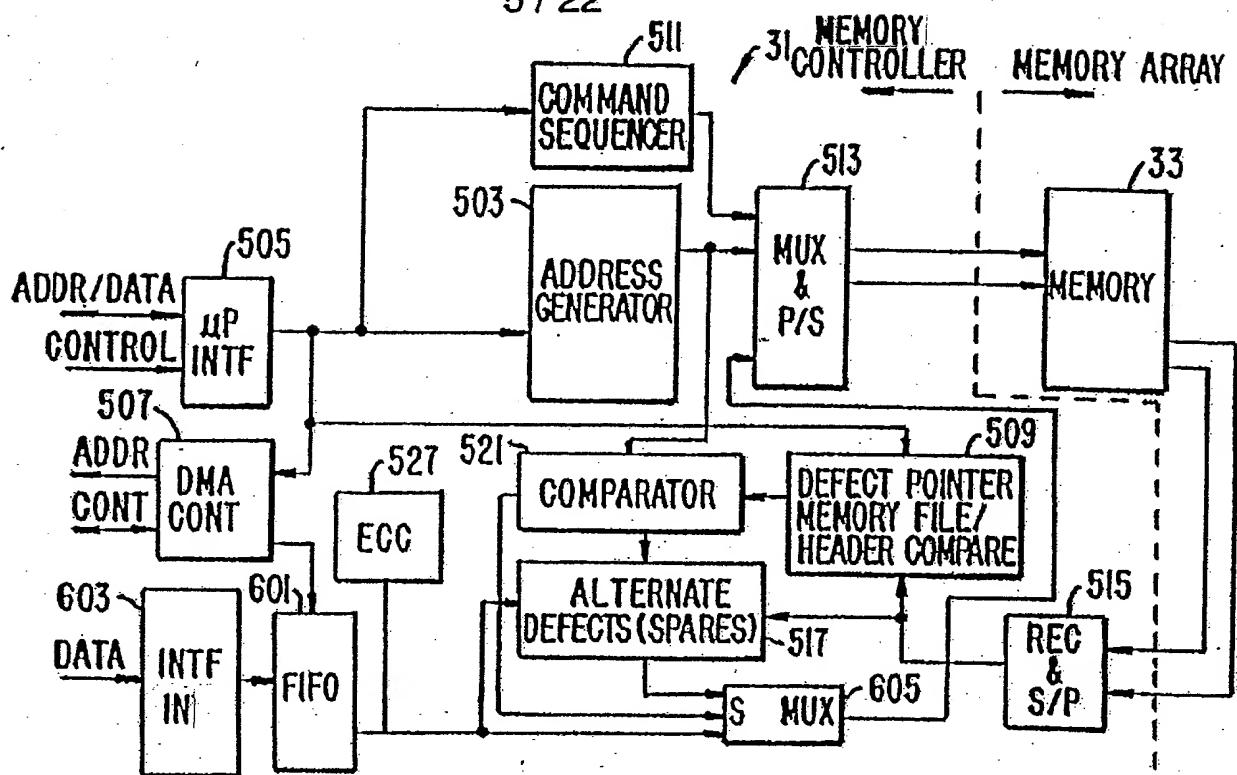
SECTOR PARTITION



READ DATA PATH CONTROL

FIG.-6

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WRITE DATA PATH CONTROL

FIG.-7

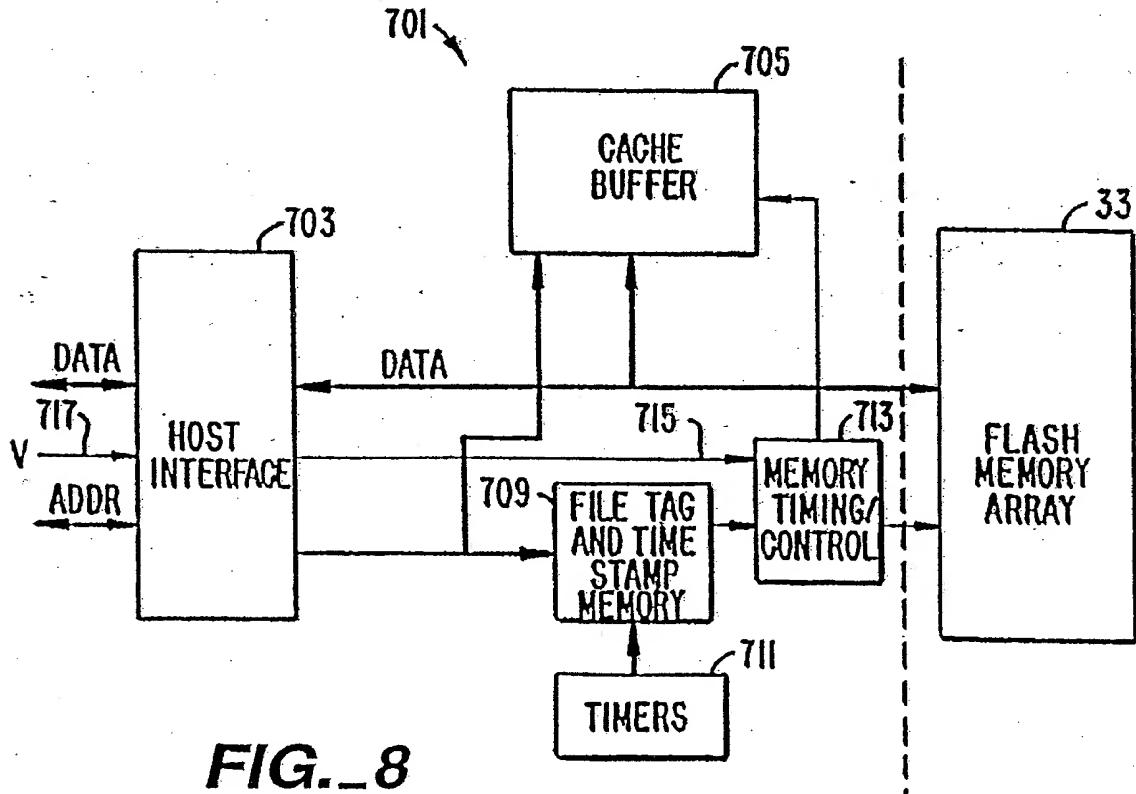


FIG.-8

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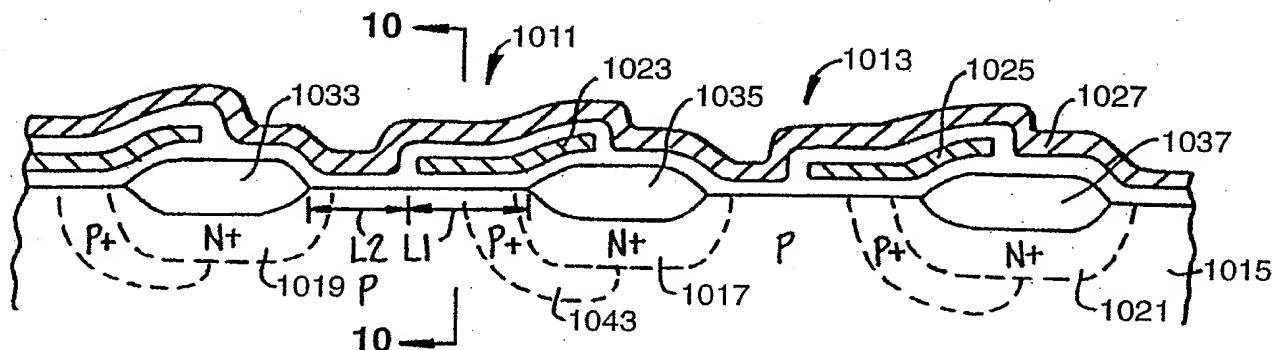


FIG._9

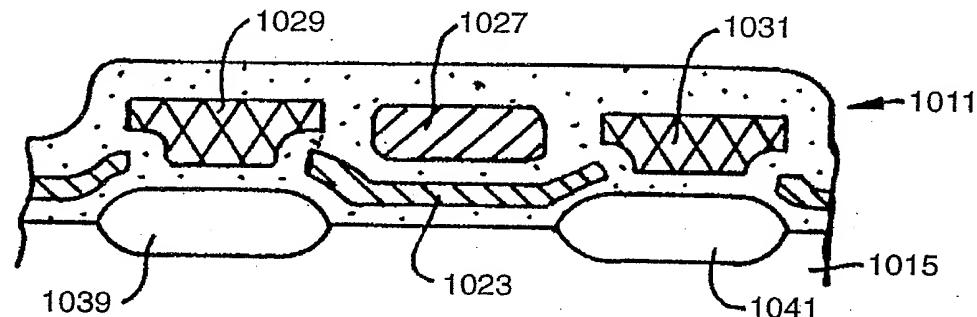


FIG._10

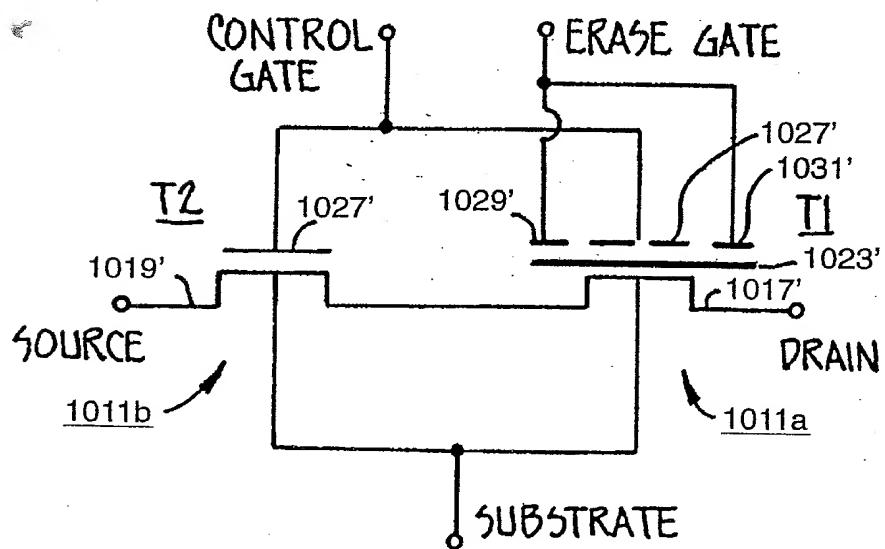


FIG._11

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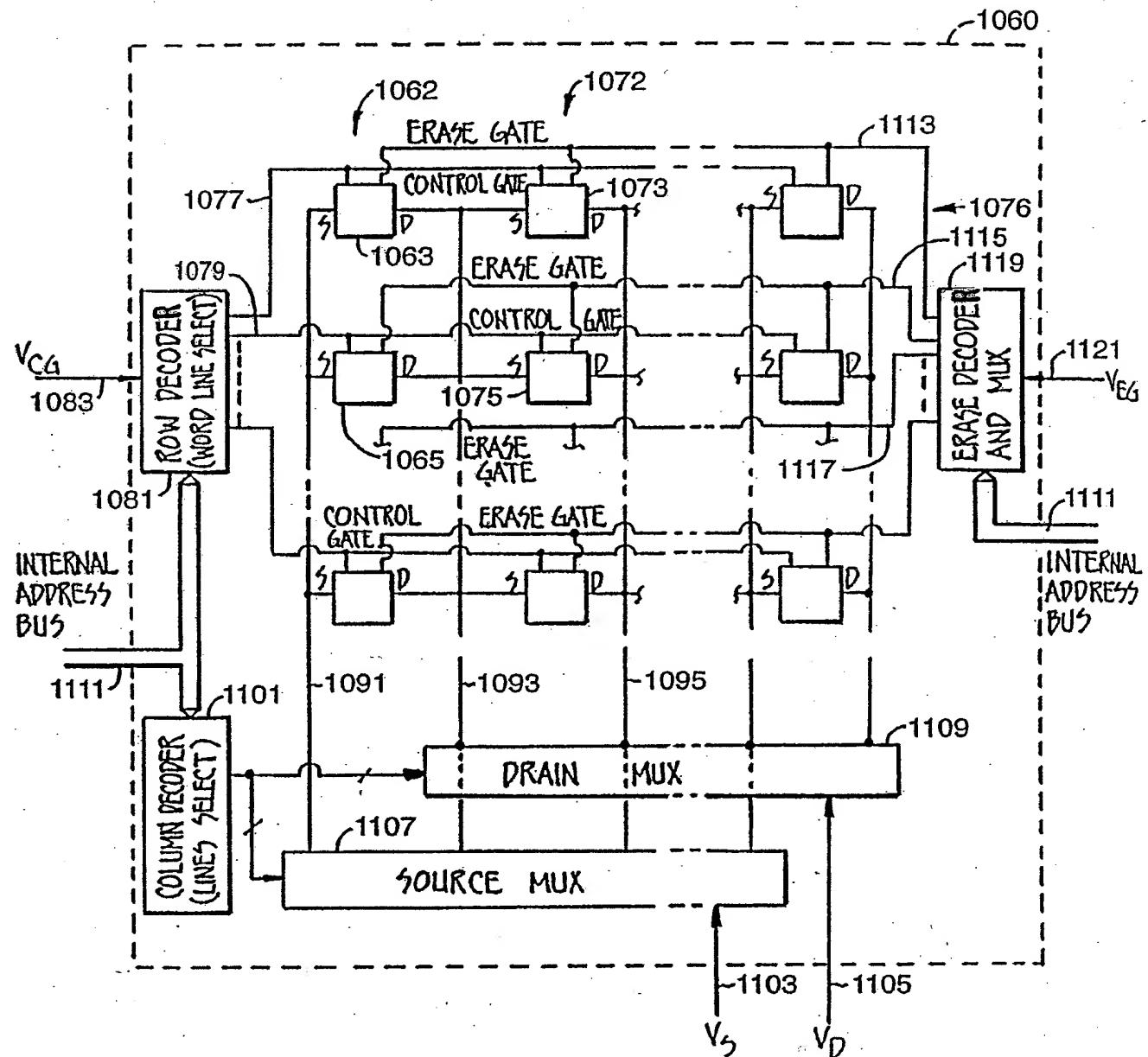


FIG. 12

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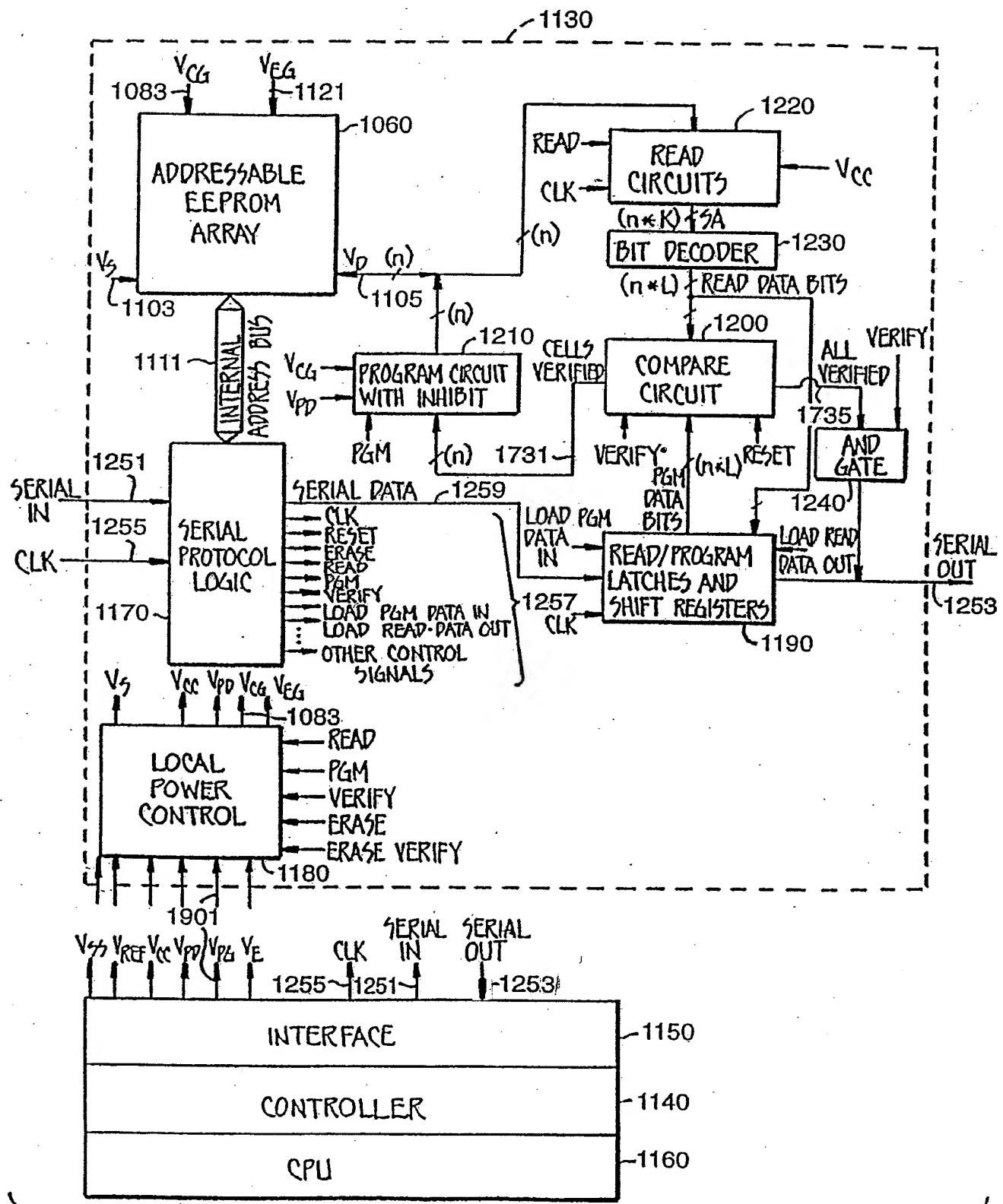


FIG. 13

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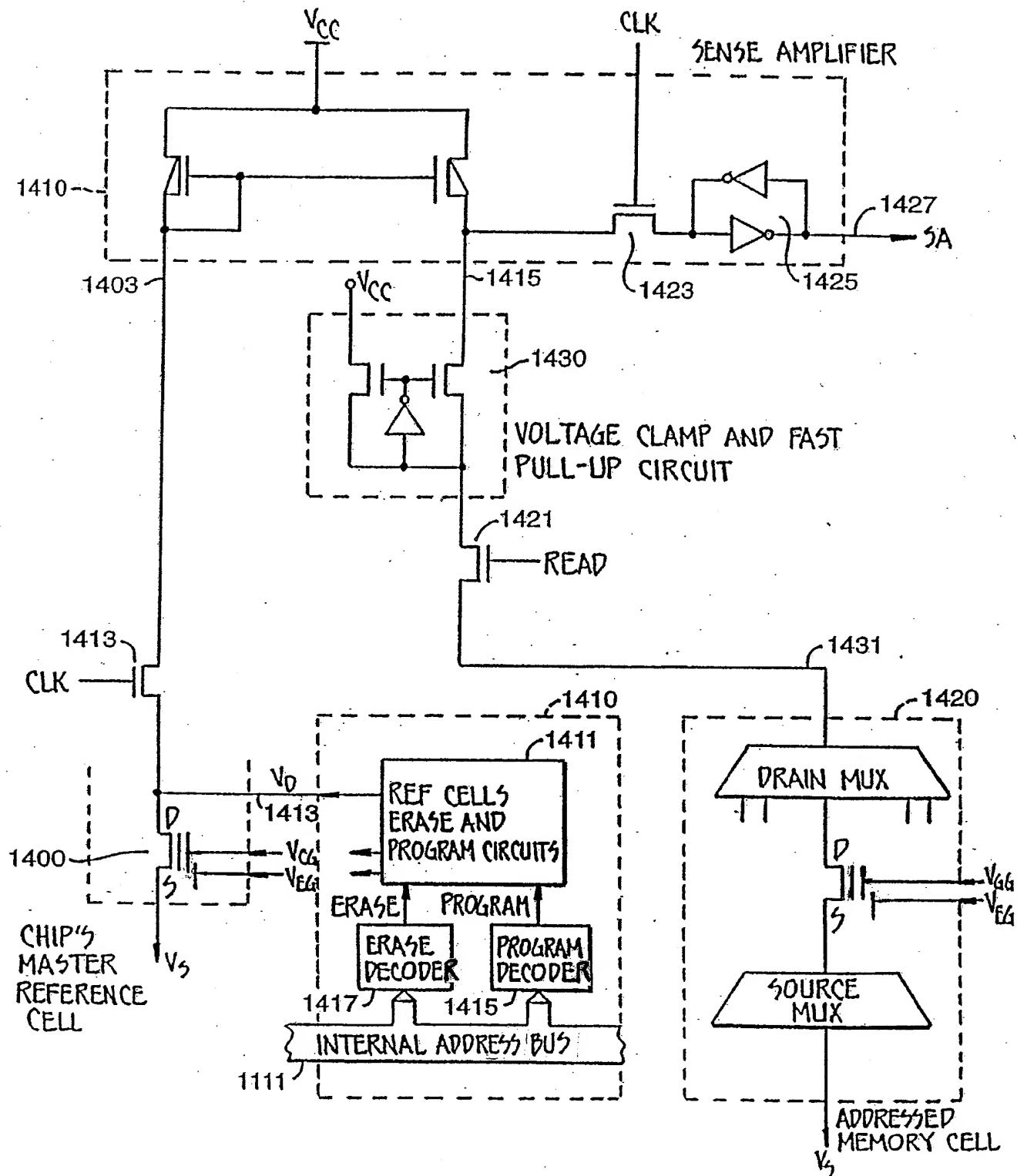


FIG. 17A

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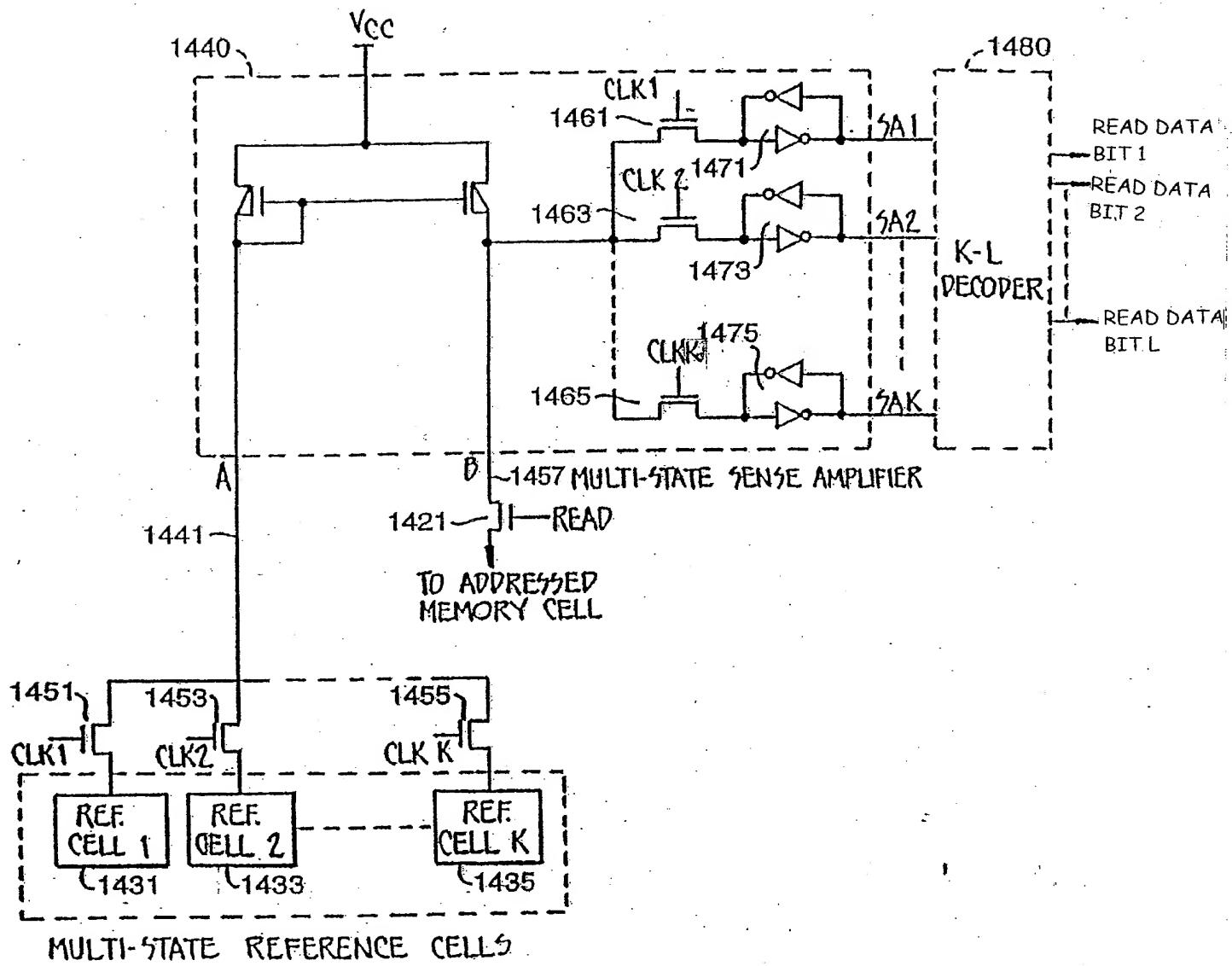


FIG. 17B

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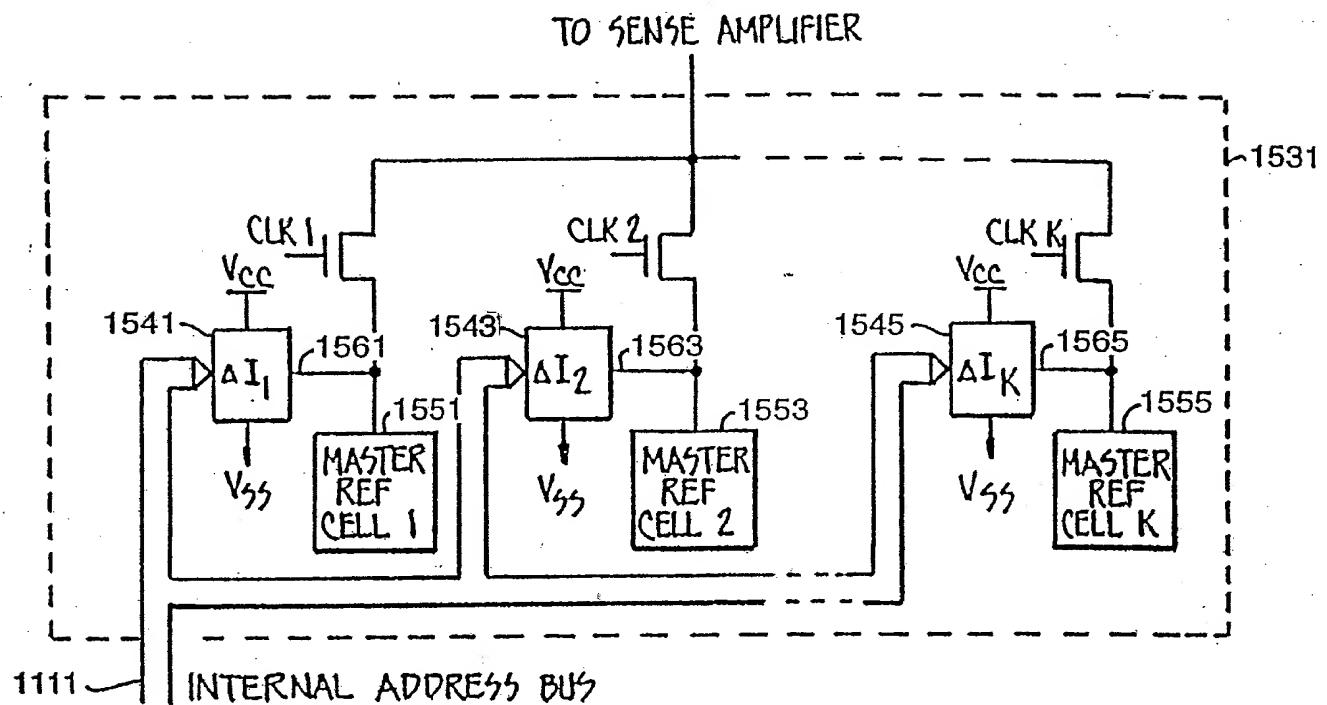


FIG. 21B

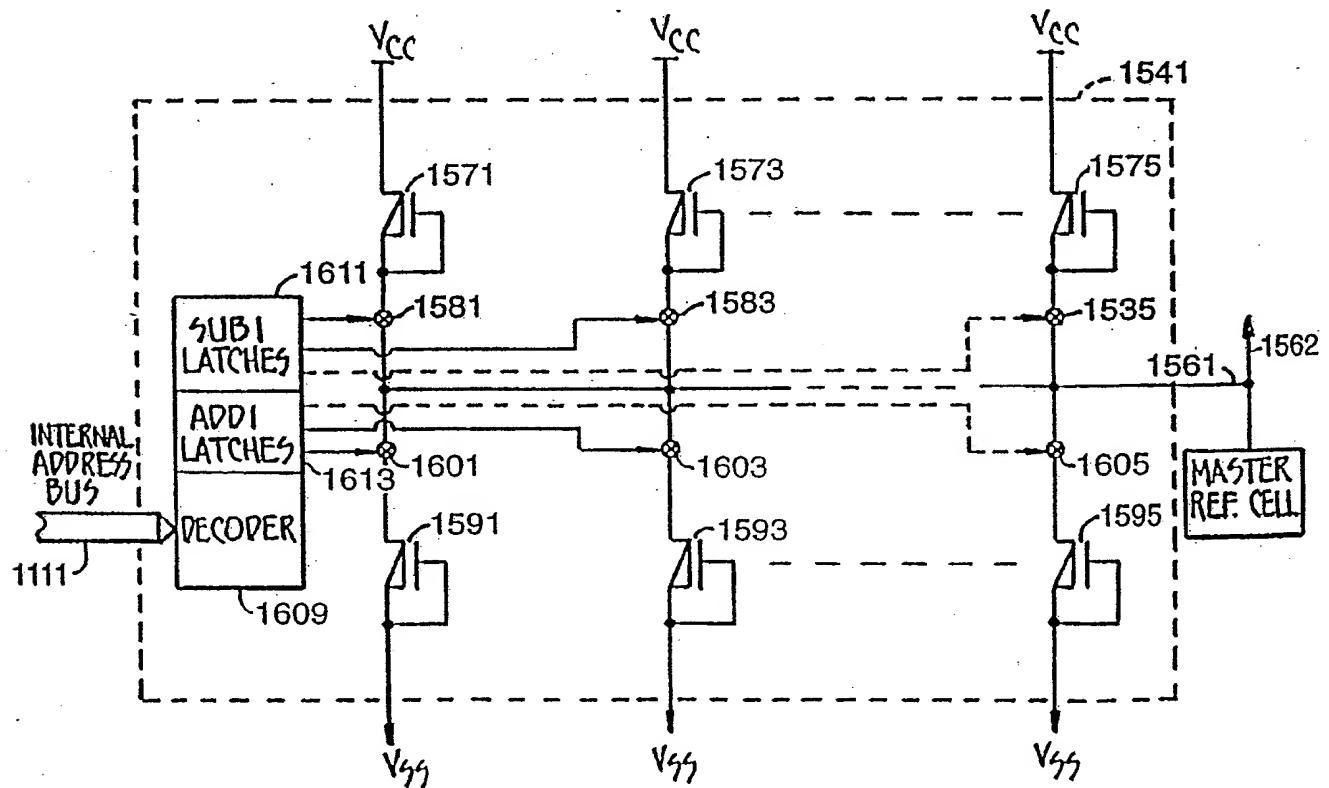
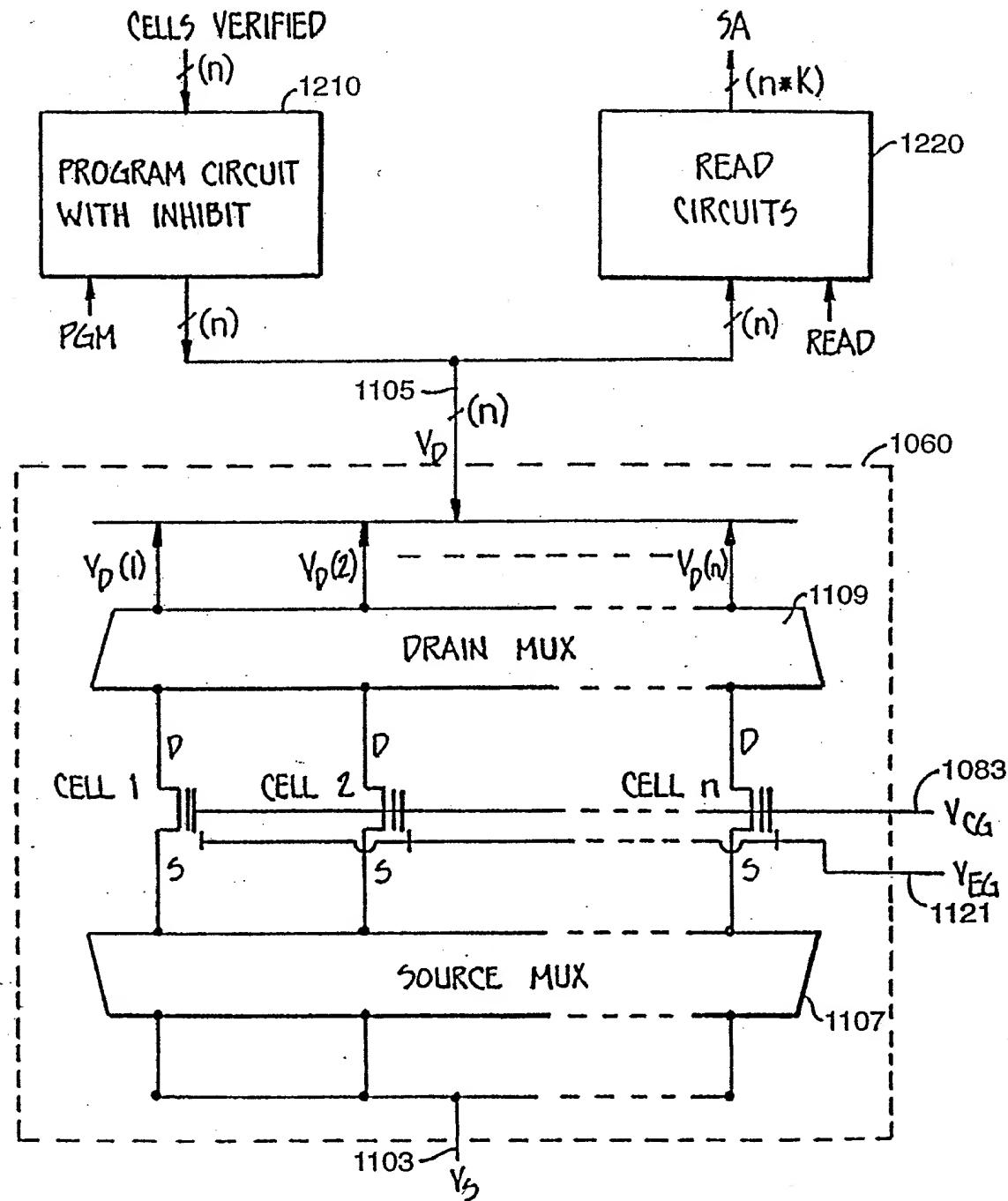


FIG. 21C

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READ/PROGRAM DATA PATHS FOR n CELLS IN PARALLEL

FIG.-22